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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1
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1

of 2

2

**Complete if Known**

Application Number

	N/A
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Filing Date

Herewith

**First Named Inventor**

Paul Gerard D'Arcy

## Art Unit

	N/A
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**Examiner Name**

	N/A
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Attorney Docket Number

D'ARCY 15-6-7

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner  
Signature**

*A. Stechen Pal dine*

Date	Considered
10/1/80	10/1/80
10/2/80	10/2/80
10/3/80	10/3/80
10/4/80	10/4/80
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10/31/80	10/31/80

5 January 2006

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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		Application Number	N/A
		Filing Date	Herewith
		First Named Inventor	Paul Gerard D'Arcy
		Art Unit	N/A
		Examiner Name	N/A
		Attorney Docket Number	D'ARCY 15-6-7
Sheet	2	of	2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
R50		C. Perkins; Minimal Encapsulation Within IP; RFC 2004; October 1996.	
R50		K. Bickerstaff, et al.; Parallel Reduced Area Multipliers; Journal of VLSI Signal Processing; Vol. 9; Kluwer Academic Publishers, Boston; 1995; pp. 181-192.	
R50		I. Koren; Computer Arithmetic Algorithms; Prentice-Hall, Inc.; 1993; pp. 71-72 and pp. 108-111.	
R50		J. Postel; Internet Protocol; RFC 791; University of Southern California, Information Sciences Institute; September 1981; pp. 1-45.	
R50		J. Postel; Transmission Control Protocol; RFC 793; University of Southern California, Information Sciences Institute; September 1981; pp. 1-85.	
R50		J. Postel; I User Datagram Protocol; RFC 768; August 1980; pp. 1-3.	
R50		L. Dadda; Some Schemes For Parallel Multipliers; Alta Frequenza; Vol. 34; pp. 349-356; 1965.	
R50		C.S. Wallace; A Suggestion For a Fast Multiplier; IEEE Transactions on Electronic Computers; February 1964; pp. 14-17.	

Examiner Signature	<i>R. Stephen Dildine</i>	Date Considered	5 January 2006
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